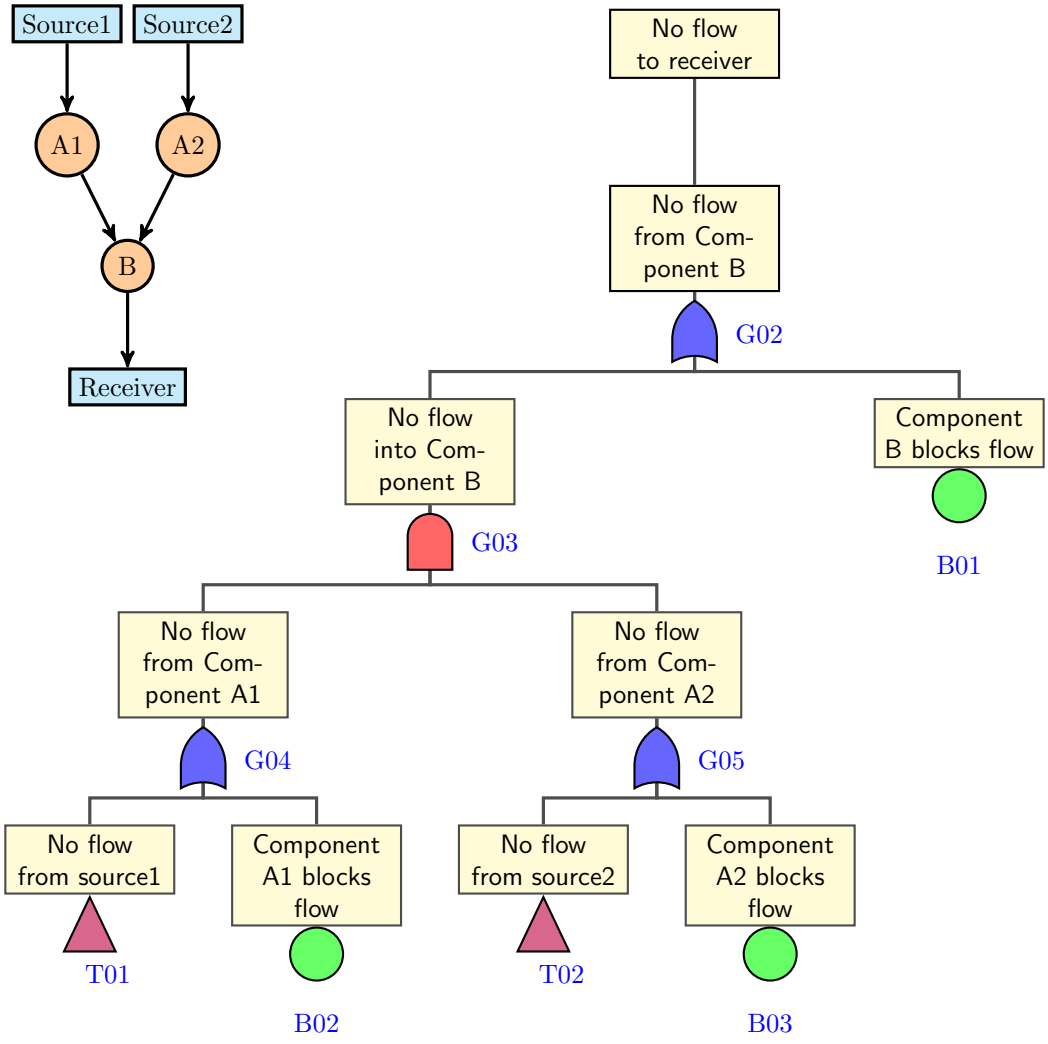
Source1
Source2
A1
A2
B
Receiver
No flow to receiver
No flow from Component B
G02
No flow into Component B
Component B blocks flow
G03
B01
No flow from Component A1
No flow from Component A2
G04
G05
No flow from source1
Component A1 blocks flow
No flow from source2
Component A2 blocks flow
T01
T02
B02
B03